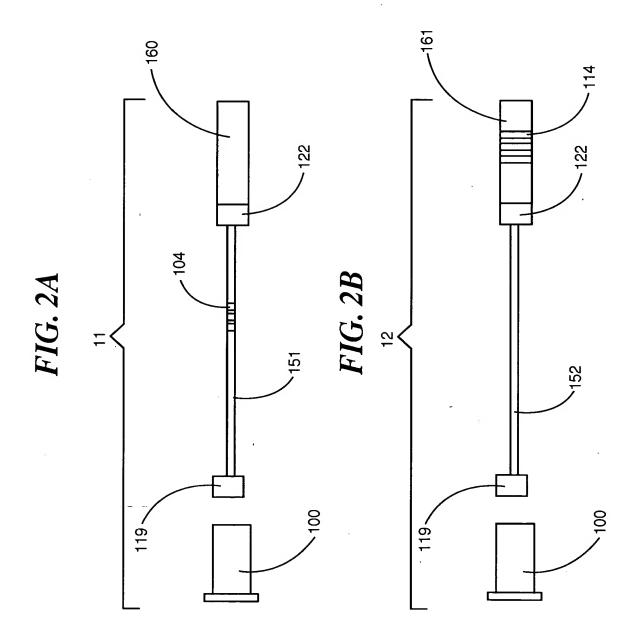
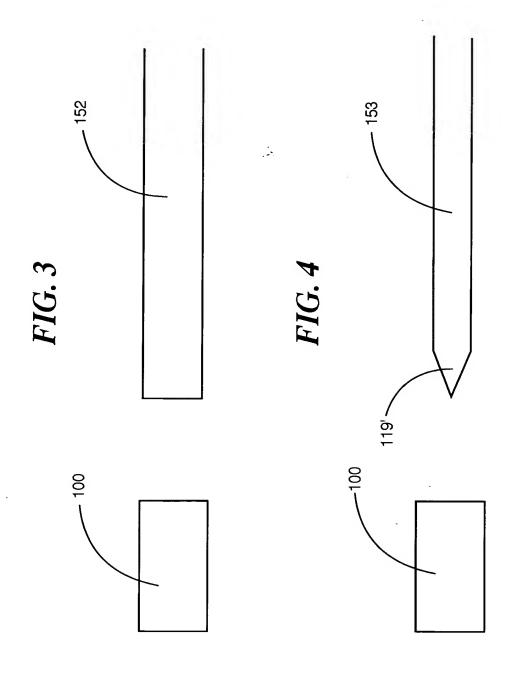
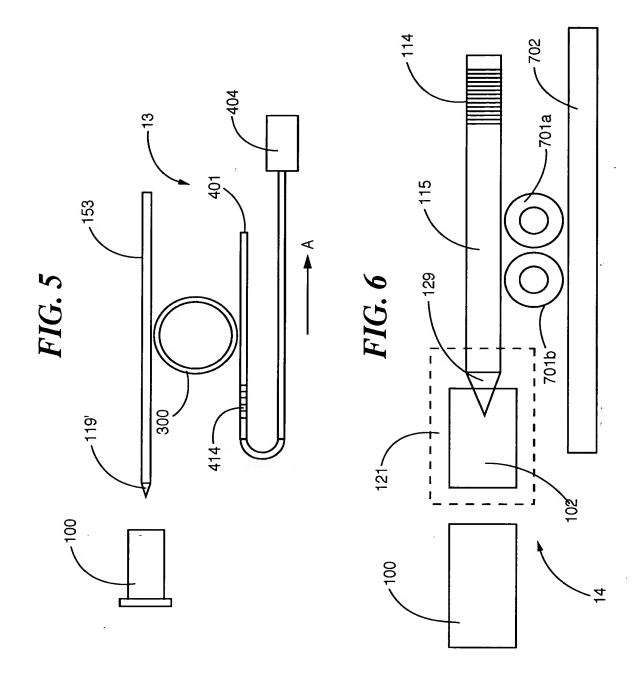
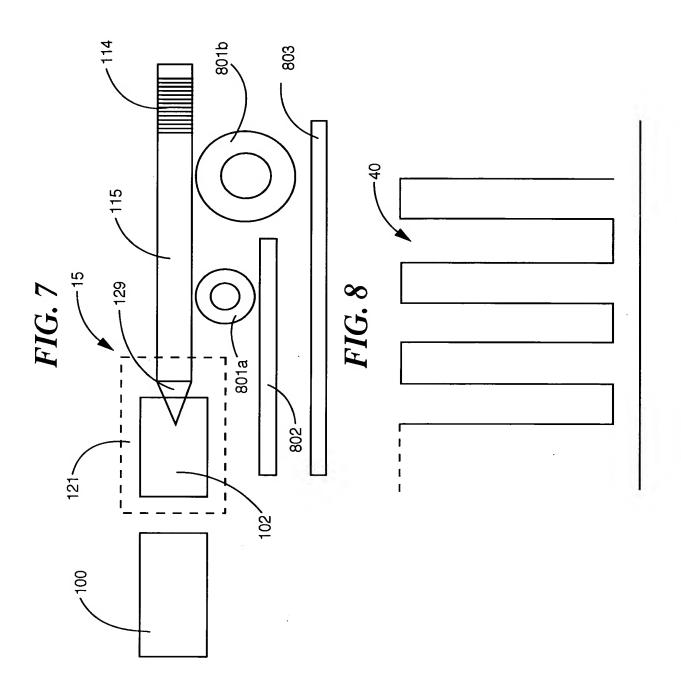


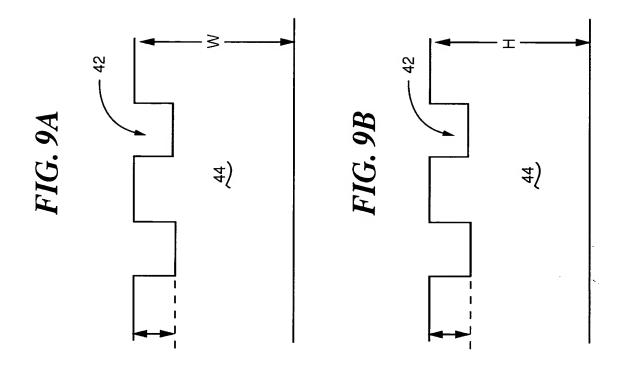
FIG. 1

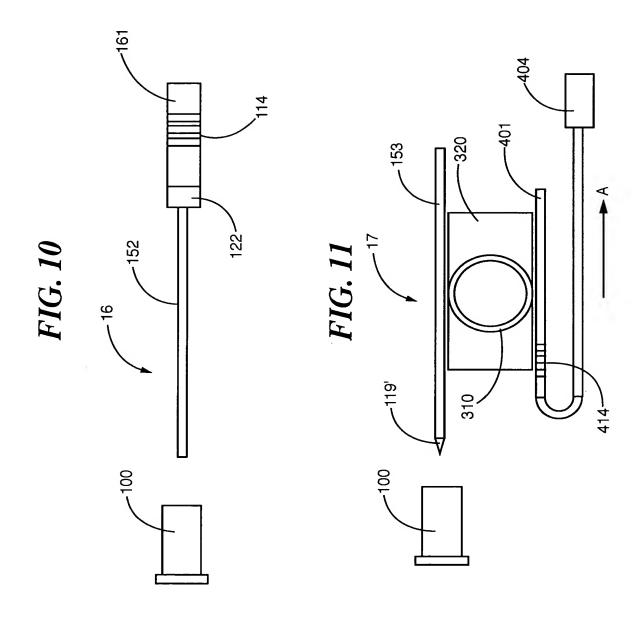


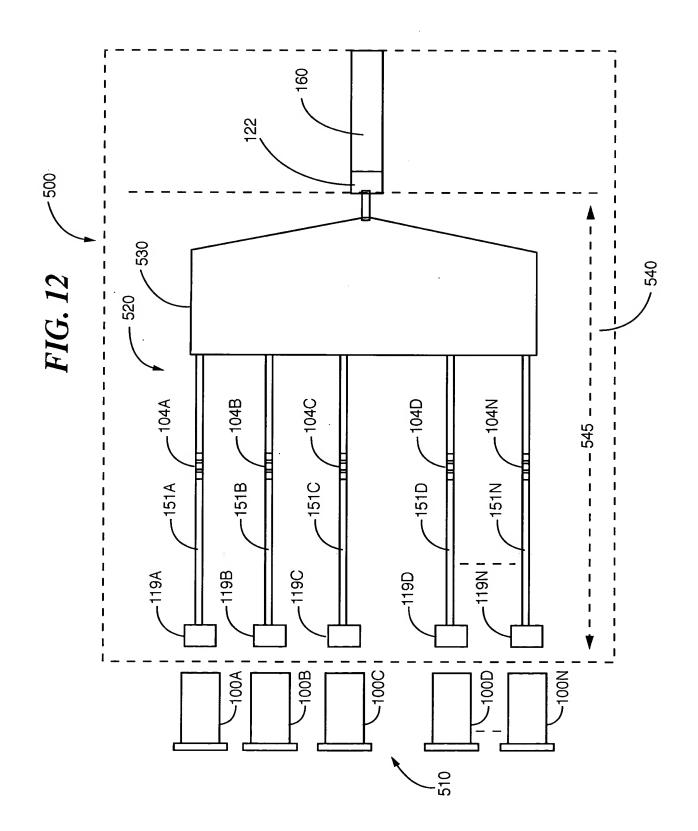


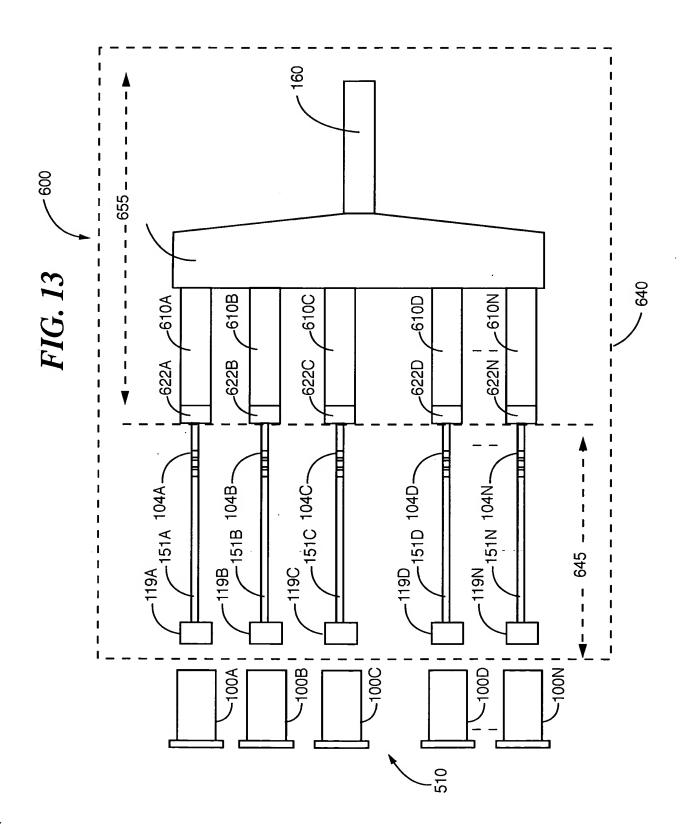


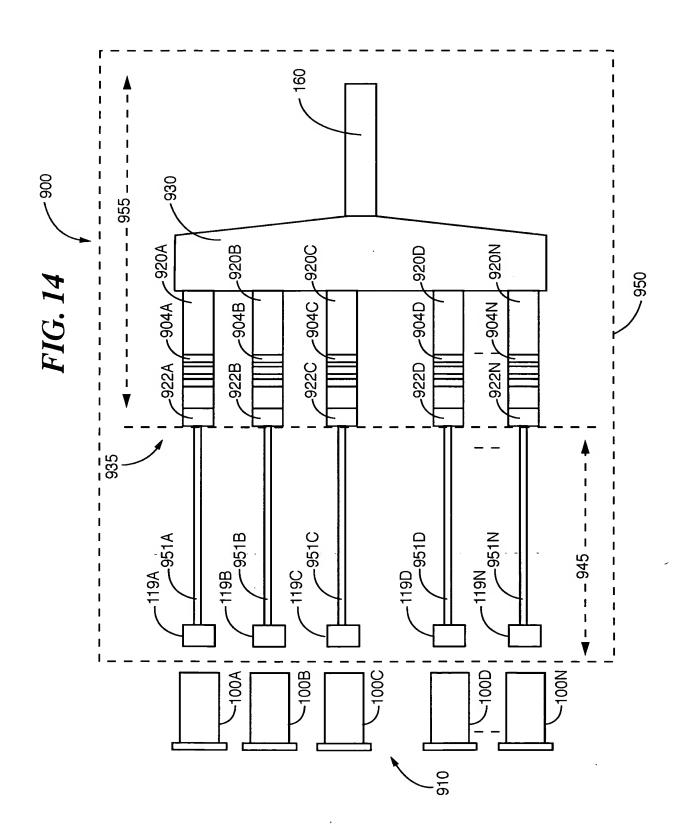


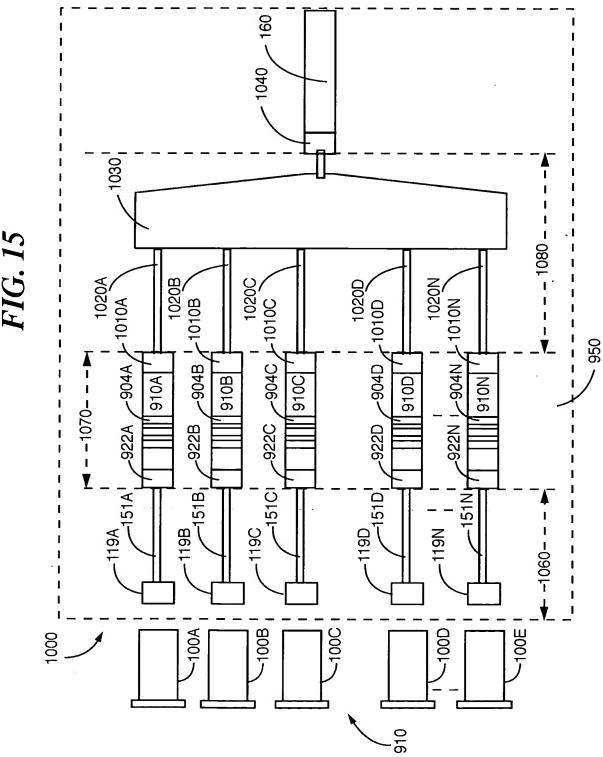


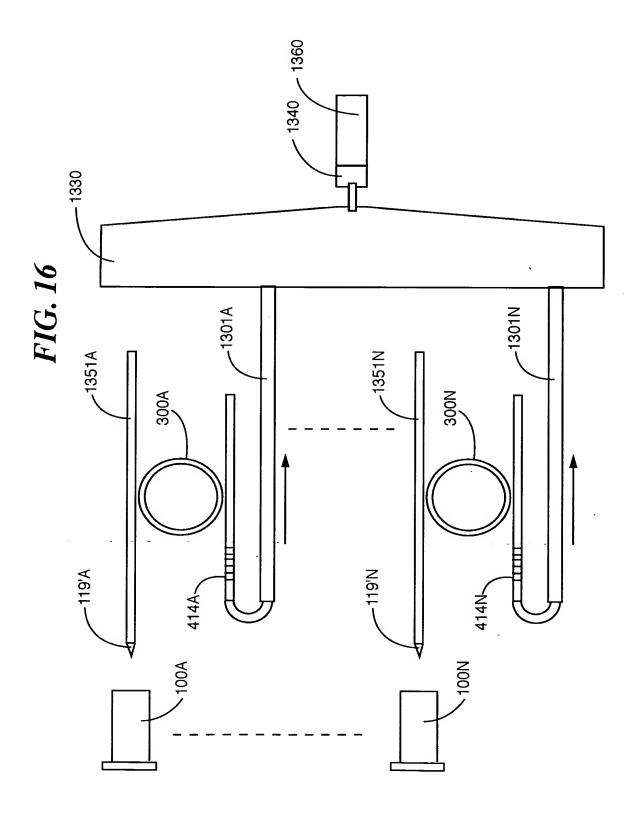












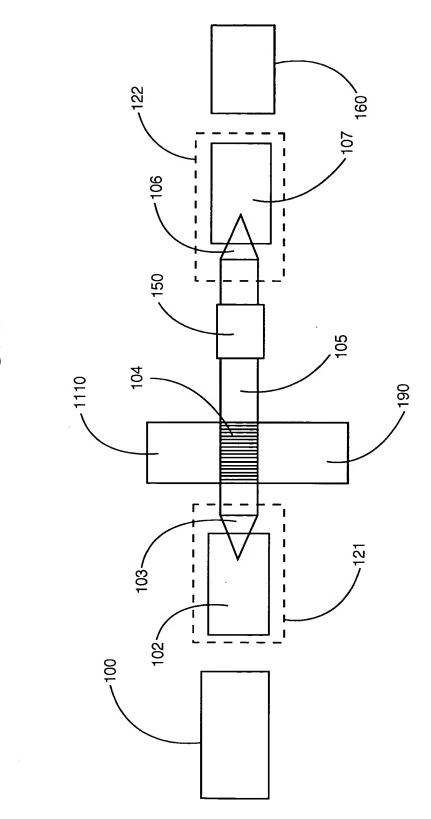
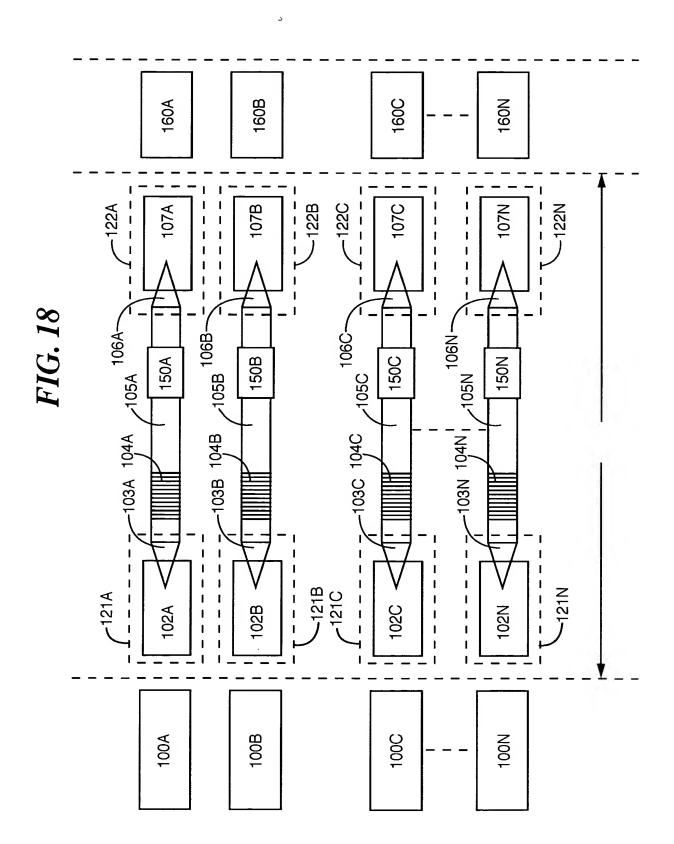


FIG. 17



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FIG. 19

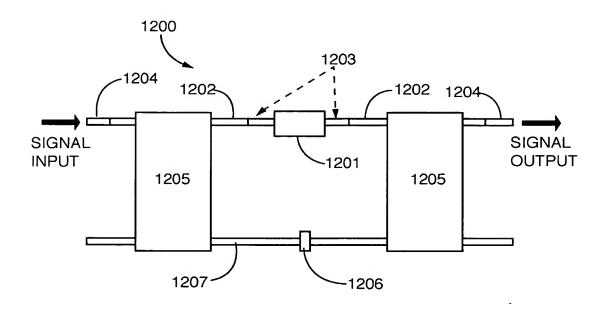


FIG. 20

1300

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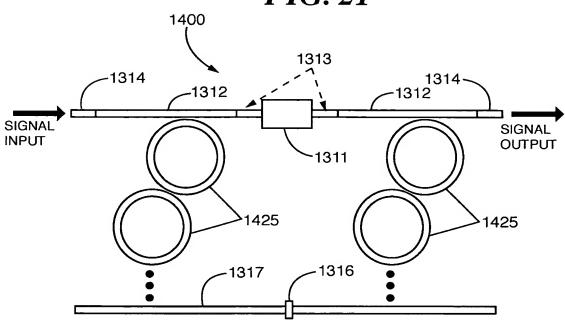
SIGNAL
INPUT

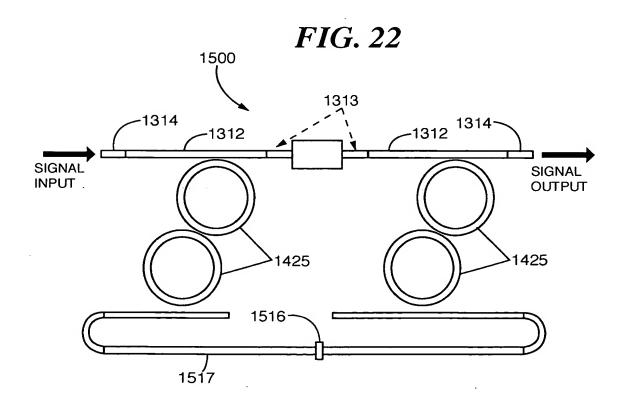
1315

SIGNAL
OUTPUT

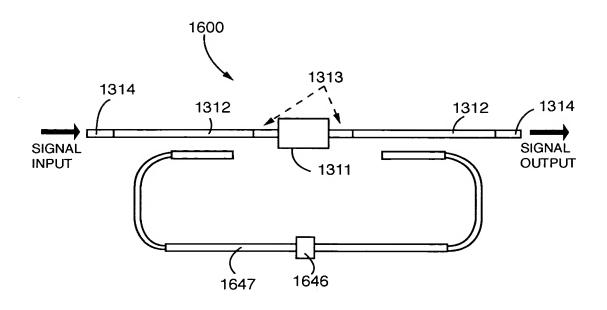
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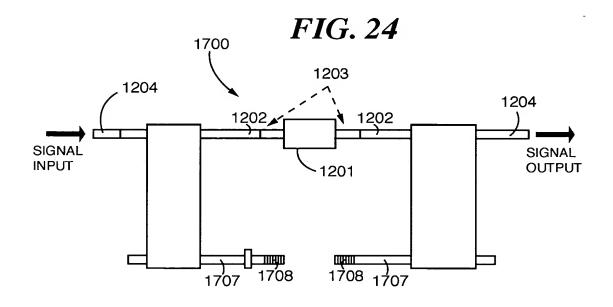
FIG. 21





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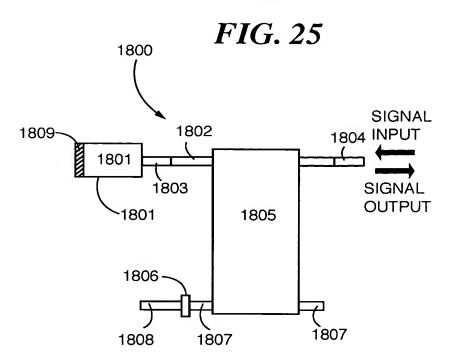
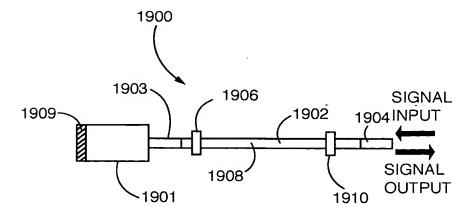
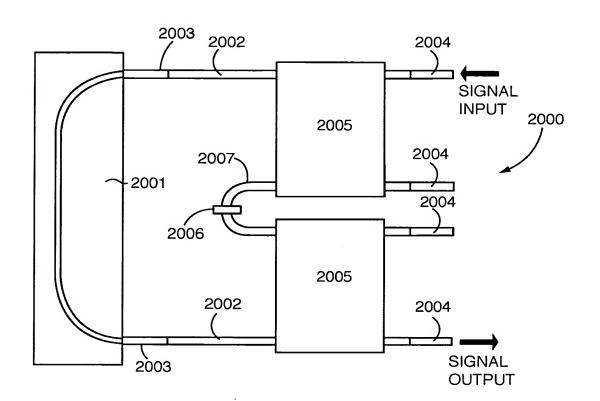
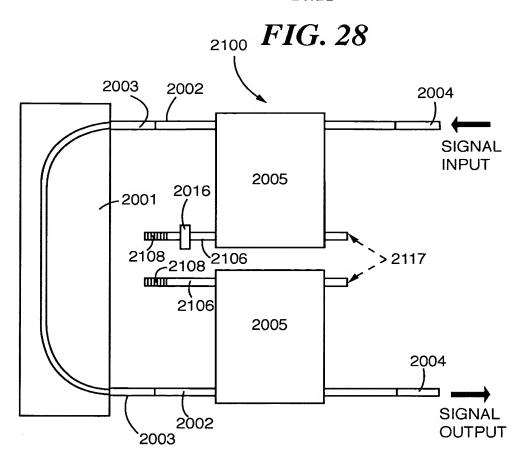


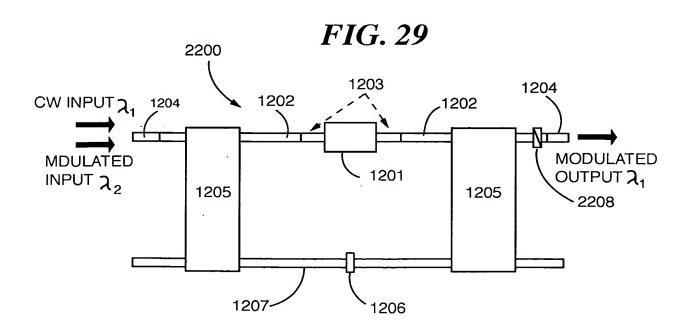
FIG. 26



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FIG. 30

